

B99-CU-R-05
REYQ26XA1YDA

3/8 x
7/8 x
3/4"

999-55B-1-05
BSF9254TVJ

A

B

C

D

E

F

Client	USA
Project	Daycare center
Title	Controller wiring schematics Control Group
Date	04/29/2022
Drawing No	

899-CU-R-01
REV120KAYDA

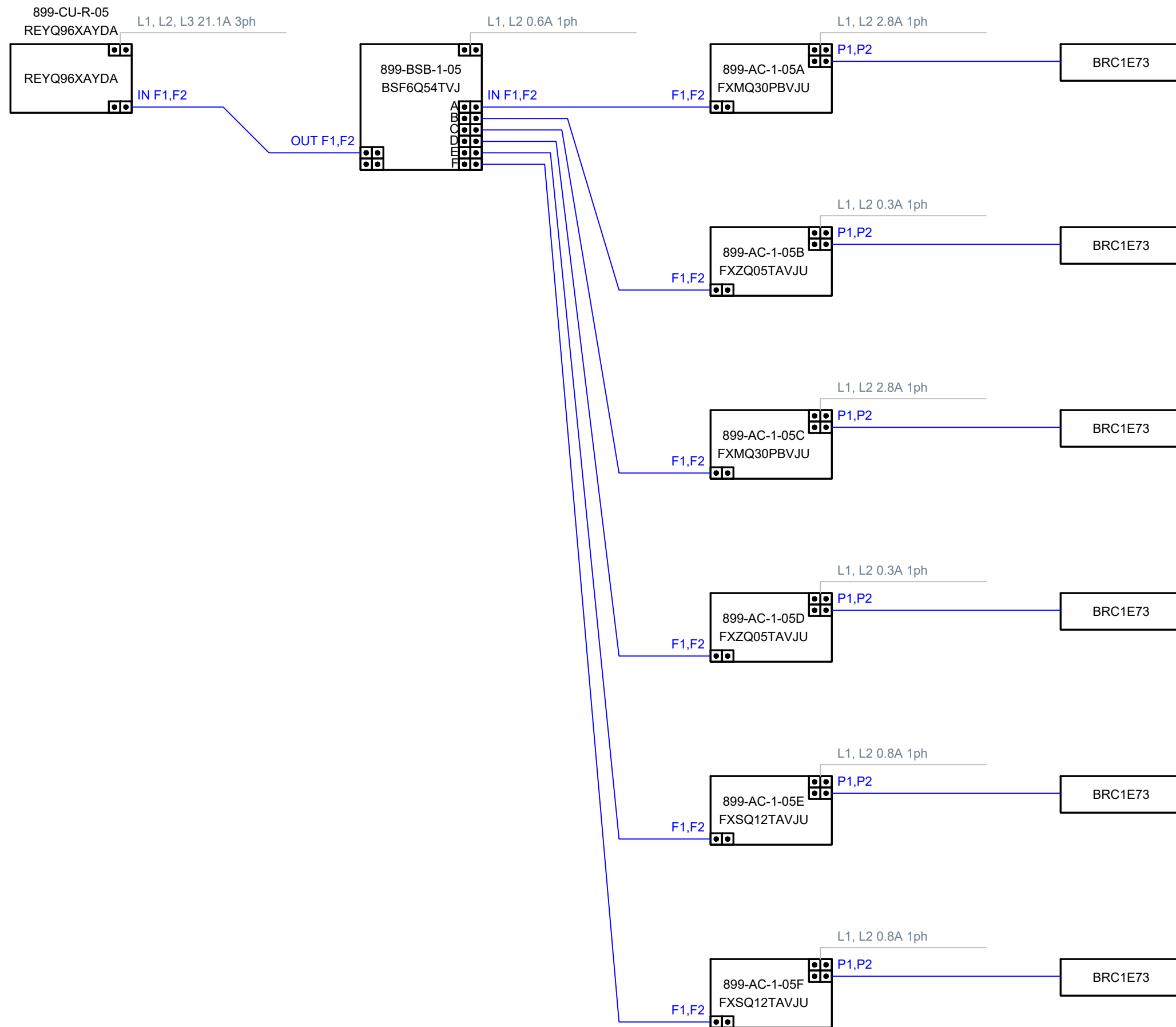
899-CU-R-02
REV120KAYDA

899-CU-R-03
REV120KAYDA

899-CU-R-04
REV120KAYDA

899-CU-R-05
REV096KAYDA

Figure 1: Block diagram of the test setup. The diagram shows two integrated circuits (ICs) connected by a signal line. The first IC is labeled "809-CL1-R-05 REV D26KXAYDA" and has pins L1, L2, L3, 21, 1A, and 3ph. The second IC is labeled "809-R58B-1-05 B8F6Q264TVJ" and has pins L1, L2, L3, 21, 1A, and 3ph. A signal line labeled "IN F1.F2" connects the output of the first IC to the input of the second IC. The output of the second IC is labeled "OUT F1.F2".



Langan
21 Penn Plaza, 360 West 31st Street, 8th Floor
New York, NY 10001
(212) 479-5400 Phone
(212) 479-5444 Fax

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Floor

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